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By Sudakar Singh Chauhan

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Low-power side-channel attack-resistant asynchronous S-box design for AES flash ADC are replaced with CMOS inverters post-CMOS low-power

Sceas

A CMOS-Based Tactile Sensor for 1.2GSps Low-Power Flash-ADC in 0.13 μ m Digital CMOS. An Efficiently Preconditioned GMRES Method for Fast Parasitic-Sensitive

A soc based low power 8-bit flash adc in 45 cmos

A SoC based low power 8-bit flash ADC in 45 nm CMOS technology S. Guha, P. Sharma Student, Dept. of Electronics & Comm. Engg. Surendra Institute of Engineering

Flash- based 8-bit risc cmos controller

Flash-Based 8-Bit RISC CMOS Controller PT8982/PT8988 DESCRIPTION The PT8982/PT8988 microprocessor is a general purpose 8-bit processor. and low power.

Design of 45nm switched inverter scheme (sis) adcs

Design of 45nm Switched Inverter Scheme (SIS) ADCs for Low Dharmendra Mani Varma "Reduced Comparator Low power Flash ADC using 35nm CMOS Sudakar S. Chauhan,

Estimation of low power multi-supply voltage level

"Optimal Voltages and Sizing for Low Power," Intl. VLSI disk and processing power to consumers based on pre 3-bit CMOS flash analog to digital converter is

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Low Power CMOS Data Converter with SNDR analysis for High In this section the proposed 45nm CMOS technology based 8-bit flash analog to digital converter is

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CMOS; VLSI; Semiconductors; compactness and low power consumption. Authors: Mini Goel, This paper presents a circuit for 2-bit current-mode Flash ADC with

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Atmega128-16au low- power cmos 8-bit

The ATMEGA128-16AU is a low-power CMOS 8-bit microcontroller based on the AVR enhanced RISC architecture. By executing powerful instructions in a single clock cycle

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TI Comparator Based Low Power 6-bit Flash ADC in the authors propose an ultrafast CMOS flash ADC based on the 1-GSPS CMOS Flash Analog-to

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View bhanu pande's professional profile on LinkedIn. 6-Bit Low Power Flash-ADC February 2012 May 2012 Digvijay Singh Chauhan.

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Sudakar Singh Chauhan has received his M.Tech degree in VLSI Design Automation and Technique at NIT Hamirpur in 2009. Since then he has worked in CEERI Pilani in

High-speed low- power cmos flash analog-to-digital

A TIQ Based CMOS Flash A/D Converter for System-on-Chip Low-Power Flash-ADC in 0.13- m Digital CMOS, IEEE Journal of Solid-Sate . 144 Circuits,

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CMOS digital integrated circuits analysis and design, A NEW APPROACH TO DESIGN LOW
POWER CMOS FLASH A/D CONVERTER by Sudakar S. Chauhan,

A very low- power flash a/d converter based on

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Design of improved resistor less 45nm switched

Jul 11, 2013 DESIGN OF IMPROVED RESISTOR LESS 45NM SWITCHED 3-bit CMOS SIS based
flash ADC is 1 GS/s, Low Power Flash Analog to Digital Converter in

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A 555/690 msp/s 4-bit cmos flash adc using tiq

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Flash ADC March 2012 April nikhil singh chauhan.

Design of low power high speed 4-bit tiq based

Jan 21, 2015 Transcript of "Design of Low Power High Speed 4-Bit TIQ Based CMOS Flash ADC" low power, and low voltage CMOS flash ADCs for SoC applications.

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A tiq based cmos flash a/d converter for

Abstract: The area of low power and high speed designing of analog-to-digital converters (ADCs) has been a challenging issue over the last decade.

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